

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
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Yamazaki et al. )  
 )  
Serial No.: )  
 )  
Filed: Herewith )  
 )  
For: Semiconductor Device And Manufacturing )  
Method Thereof )  
 )  
Examiner: )  
 )  
Art Unit: )

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**PRELIMINARY AMENDMENT A**

Prior to examination, please enter the following amendment in the above-identified application.